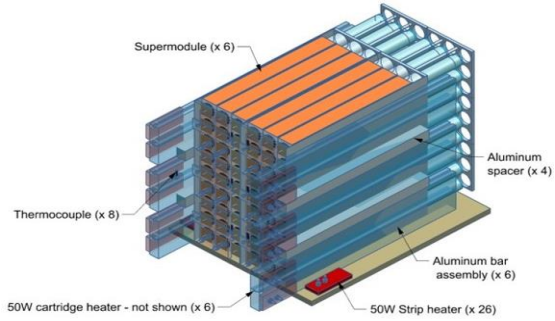




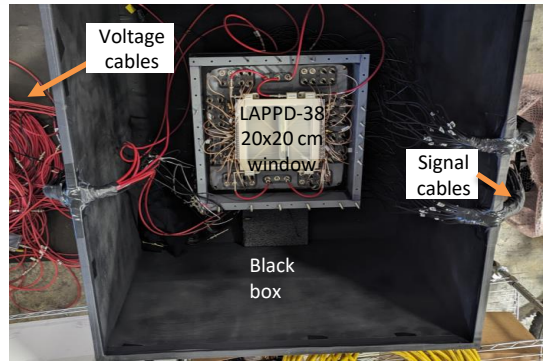
DSG Current Projects FY 2024

Dr. Patrizia Rossi
Detector Support Group
Wednesday, February 27, 2024

Contents



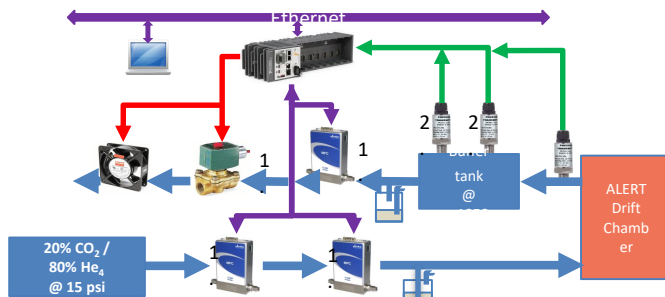
Isometric view
Hall A - ECAI



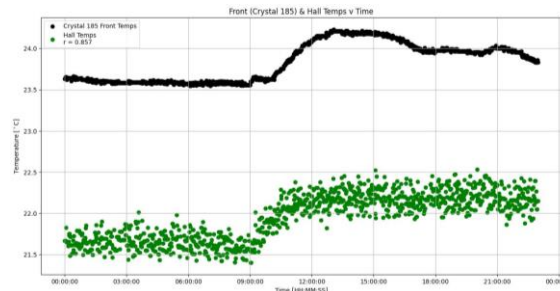
Hall A - LAPPD



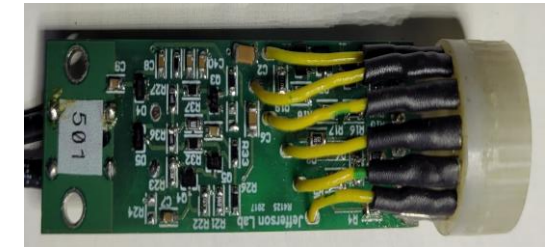
Hall A - Moller



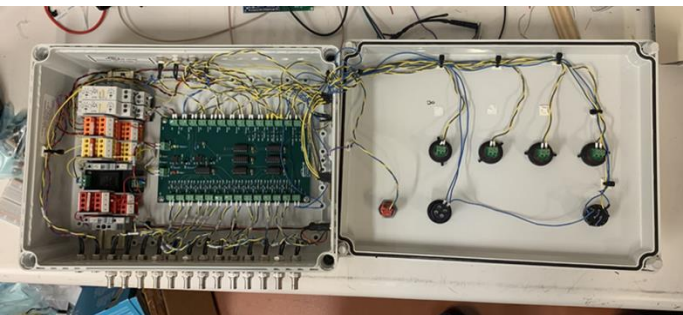
Hall B - ALERT



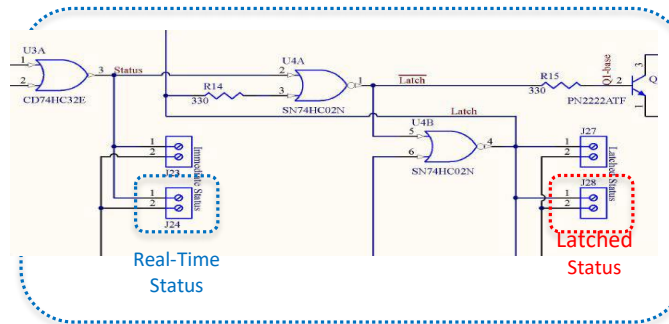
Hall C - NPS



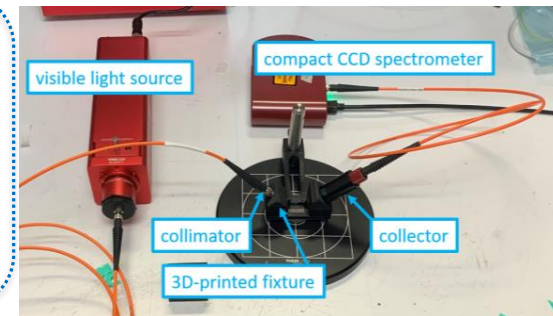
Hall D - FCAL 2



EIC- DIRC



EIC- DIRC Phoebus Alarm Handler



EIC- RICH



Hall A –ECAL

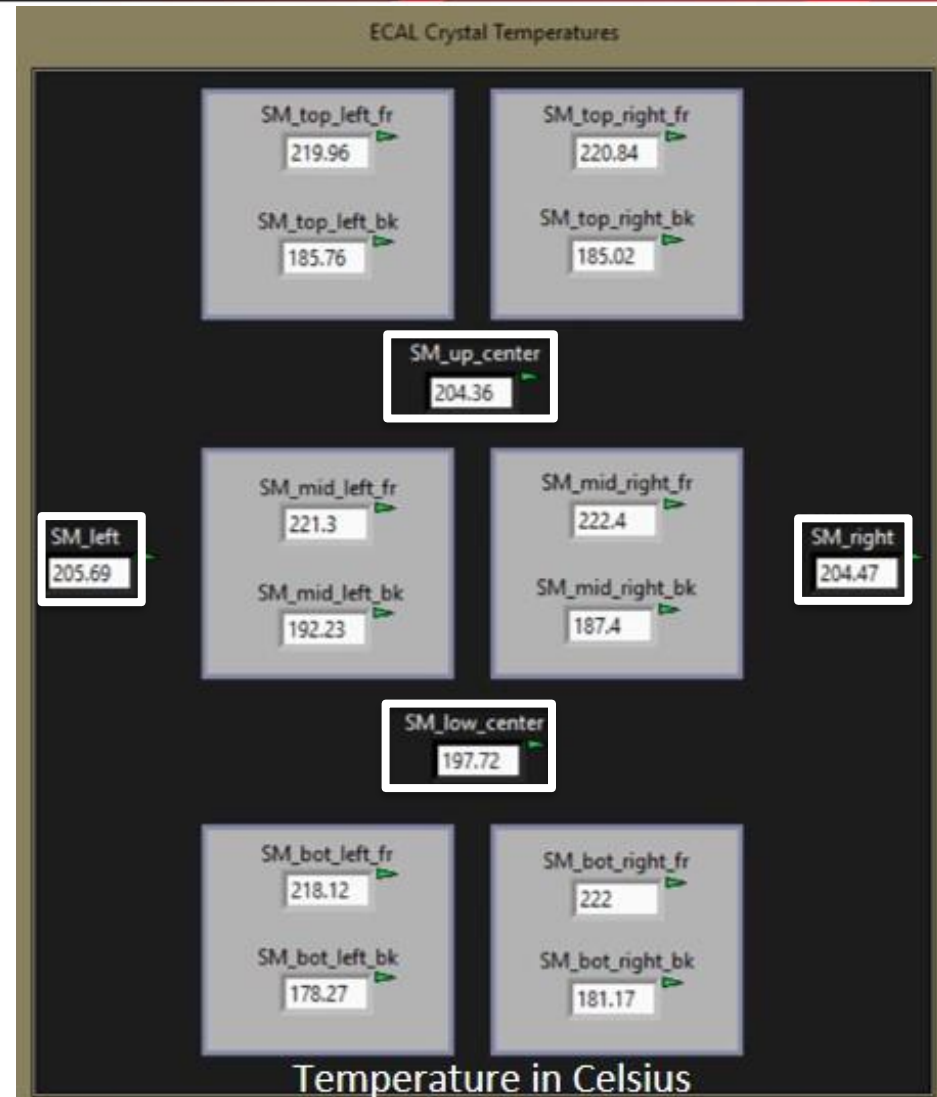
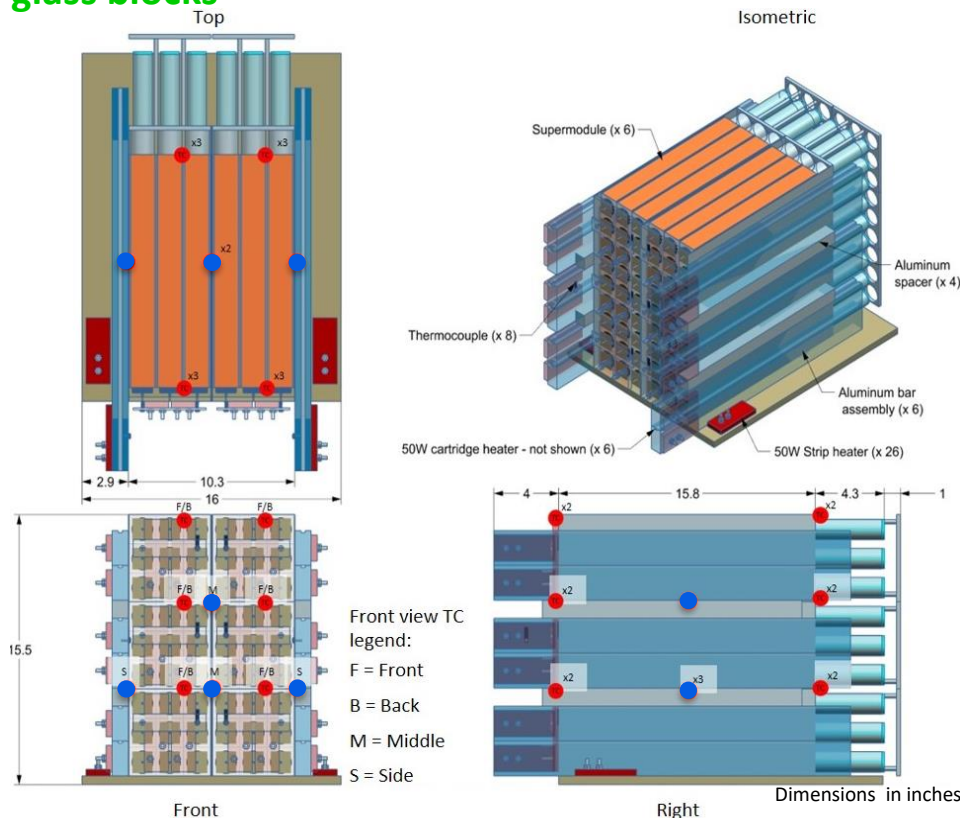


Marc McMullen, Peter Bonneau, Brian Eng, Mindy Leffel, and George Jacobs

Designed, developed, and installed prototype heater Control and Monitoring (C&M) software and GUIs for the six-supermodule test stand

Operated test stand from September to November 2023

C&M maintained ~220°C on the front surface of lead glass blocks



Thermocouples (16 in all) indicated by red dots are on front and back; thermocouples indicated by blue dots are in the middle of the lead glass block array.

Readback on GUI shows ~220°C for the front supermodules (SM) of the crystals and ~180°C for the back super modules



Hall A –ECAL

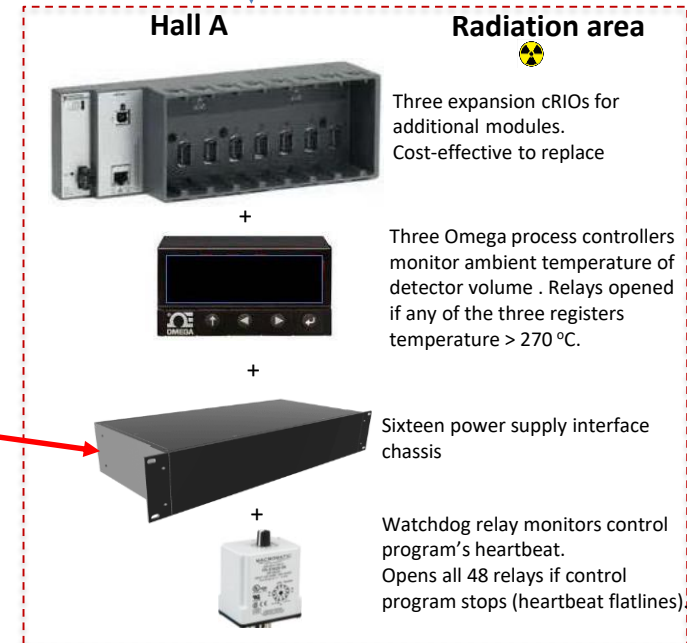
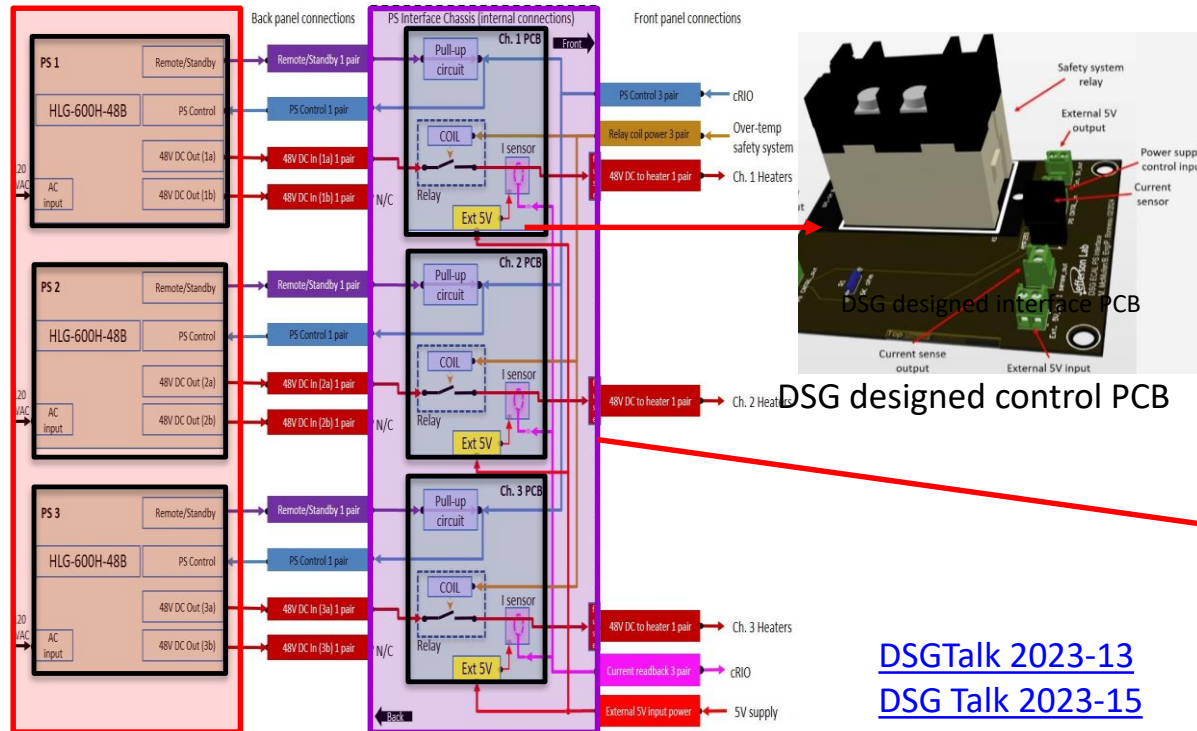
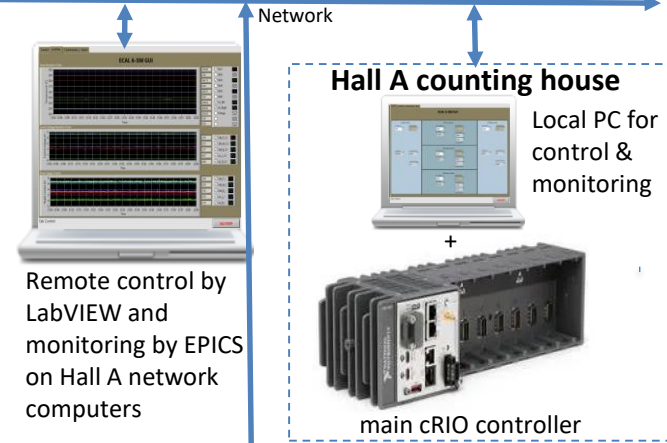
Marc McMullen, Peter Bonneau, Brian Eng, Mindy Leffel, and George Jacobs



Developing heater C&M software and GUIs for whole system

- Designing and fabricating power supply interface chassis between HV supplies and C&M system
 - Power supply interface chassis (purple box) has one interface card per HV supply
 - Procuring components for 48-channel control & monitoring system
 - Fabricating 16 power supply interface chassis for 48 HV supplies
 - Fabricating electronic components and cables

Layout of Control & Monitoring System



[DSGTalk 2023-13](#)
[DSG Talk 2023-15](#)
[DSG Note 2023-53](#)

High Voltage Supplies

power supply interface chassis

Components in Hall A endstation bunker

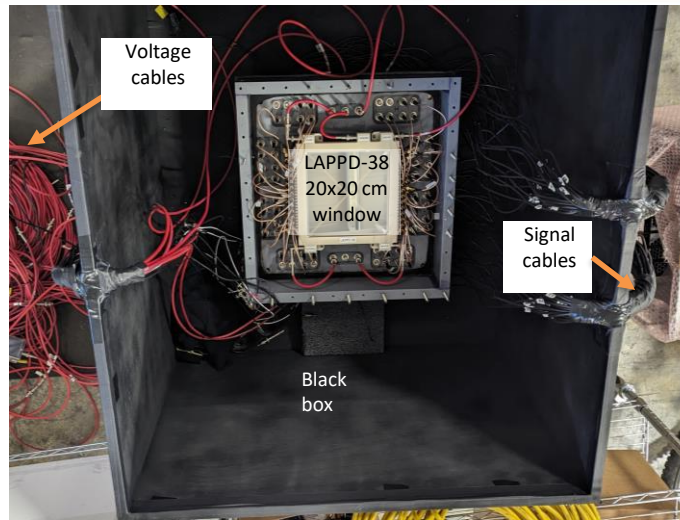


Hall A – LAPPD

Pablo Campero and Marc McMullen



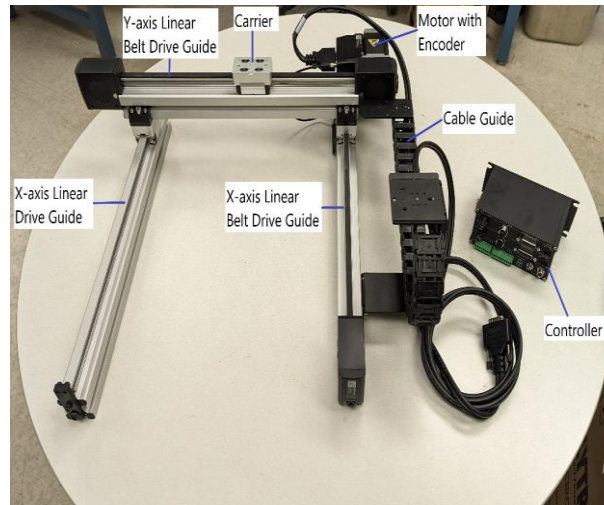
- Procured Zaber-LC40B gantry motorized positioning system to support and move LED attached to fiber
 - Linear belts (x and y) drives' guides have a motor with an embedded encoder for precise (400 μ m) movement
 - Stand-alone controller outside the detector's enclosure (black box) allows local or remote controls
- Developing C&M program and GUIs to position LED and to enable automated testing
- Assembled motorized positioning system



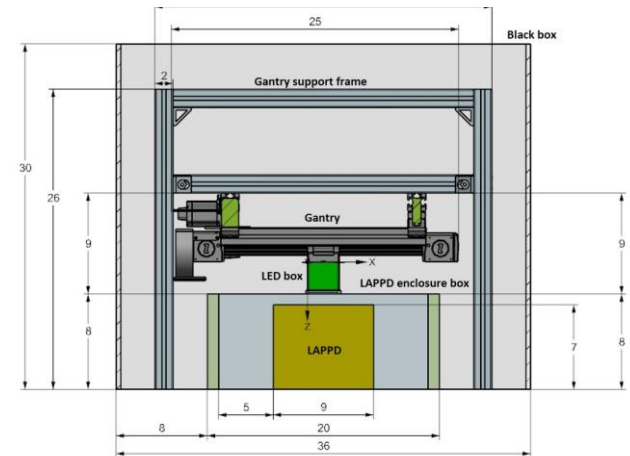
LAPPD-38 detector and components inside a black box



Marc and Pablo aligning y-linear belt drive guide with x-linear guide



Assembled motorized positioning system

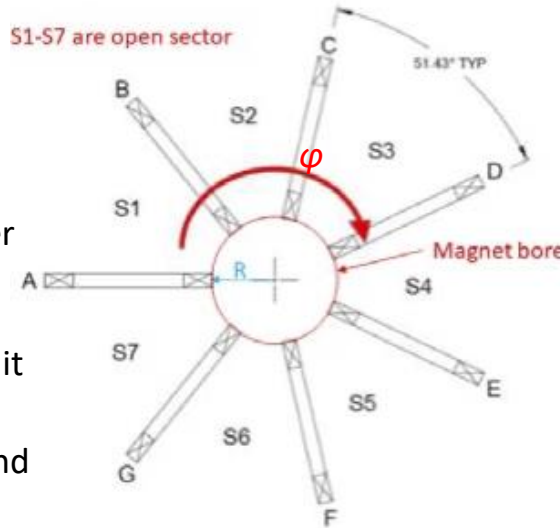
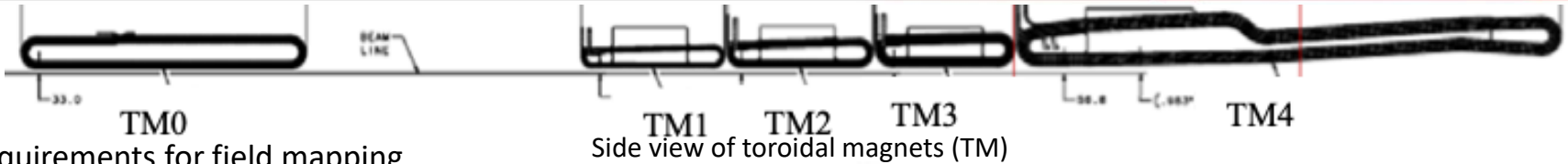


All dimensions in inches

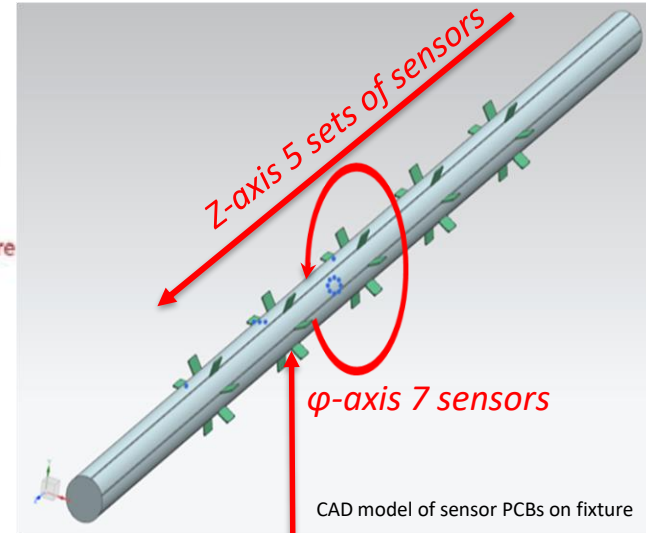
NX12 drawing of operations setup . Gantry oriented to make LED face LAPPD window

Hall A - Møller Magnets

Brian Eng, Mary Ann Antonoli, and McMullen

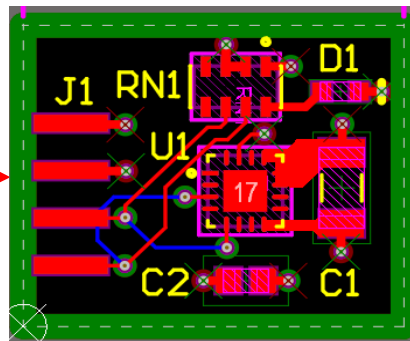
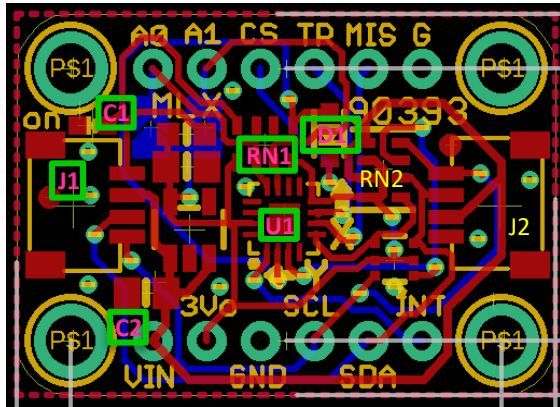


Beamline view of the seven sectors of the coil layout for all magnets

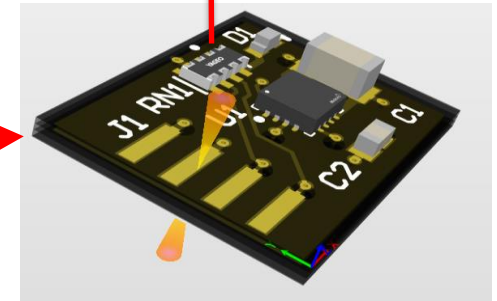


- Requirements for field mapping
 - Seven sensors in ϕ (one per sector)
 - Minimum of five sets of sensors along z
 - Sensor id: $S^{\mu\nu}$; $\mu=1,2,3,\dots,5$; $\nu=1,2,3,\dots,7$
 - Need to measure up to ~ 100 G
 - Details: [PMAG0000-0100-S0057](#)
- Selected Melexis MLX90393 magnetometer
 - Adjustable gain allows sensor full scale to be adjusted from 50 G to 500 G
- Checked magnetometer range with Adafruit development board
- DSG designed PCB reduced components and dimensions

– 2.5 cm X 1.8 cm → 1.5 cm X 1.3 cm



DSG designed PCB



Isometric view of DSG designed PCB

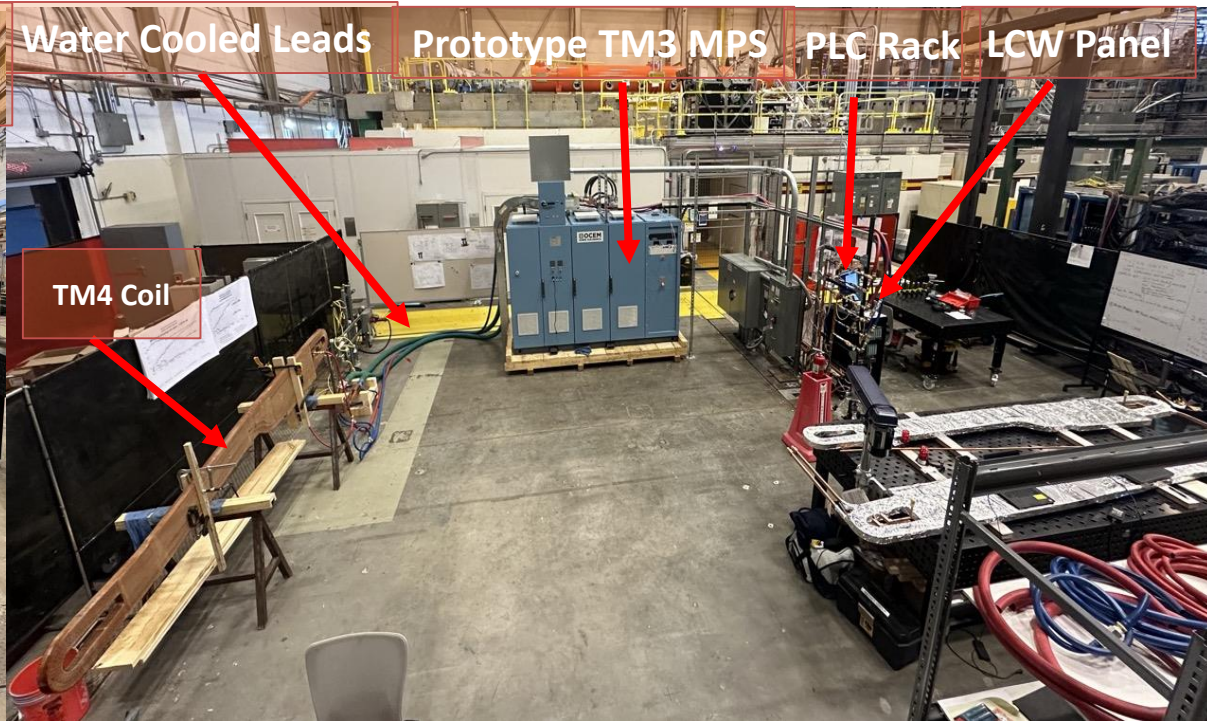
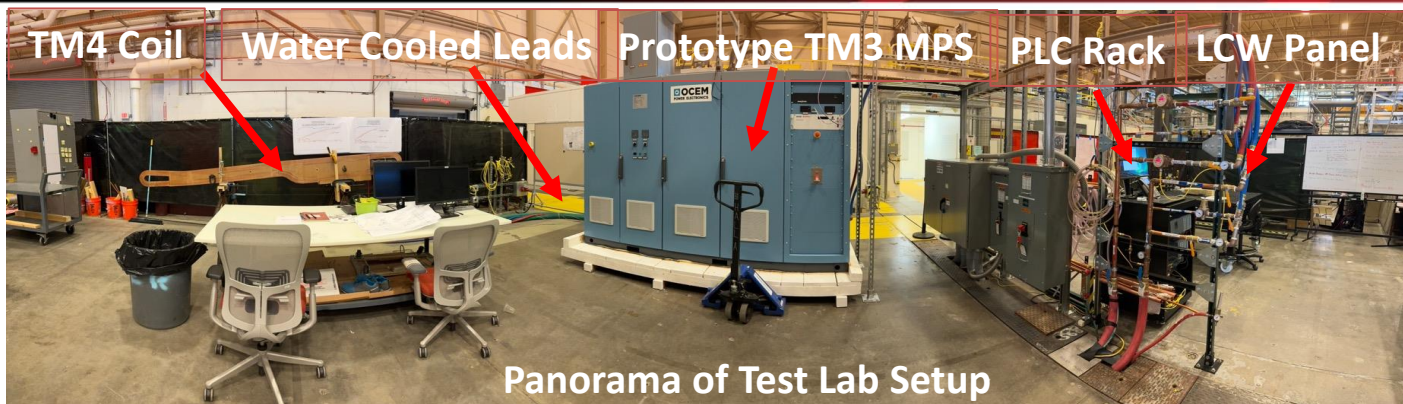
Hall A - Møller MPS

Brian Eng, Mary Ann Antonioli, and McMullen



Test Lab Instrumentation and C&M system for MPS

- Wired and instrumented sensors to PLC RTDs, VTs, flow meter, interlocks
- SoftIOc on RaspberryPi
 - Reads PLC tags and sends to EPICS PVs
- Able to communicate with MPS via Ethernet from PLC



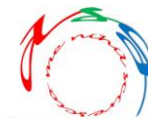
PLC Rack

LCW Rack

Overall View of Test Lab Setup

Hall A – Møller MPS

Brian Eng, Mary Ann Antonioli, and Marc McMullen



- For Site Acceptance Test TM3 MPS prototype connected to TM4 coil

– Analyzed data

Moller Test Lab Sensors

MPS LCW Flow	11.26 lpm	●	PLC Hard Interlock	FAULT
MPS LCW Temp	30.06 °C	●	PLC Soft Interlock	FAULT
Coil LCW Flow	15.46 lpm	●	Reset PLC Interlocks	<input type="checkbox"/>
Coil LCW Temp	30.35 °C	●	RTDs	
ZFCT Current	330.44 A		Ambient	22.70 °C
MPS Current	0.000 A		Coil A - Supply	31.20 °C
MPS Voltage	0.000 V		Coil A - Return	31.50 °C
			Coil B - Supply	31.20 °C
			Coil B - Return	31.40 °C
			WCL - Return	30.70 °C
			LCW Supply	31.50 °C

Voltage Taps

MPS	-0.01 V
WCL	-0.01 V
Coil A	-0.02 V
Coil B	-0.01 V

FAST DUMP PLC Heartbeat 2024-01-31 12:04:23

Phoebus screen for site acceptance test

TM3 MPS Control COMM

turn off MPS

stop ramp

Interlock Summary

Voltage Taps

VT## VT##

Temperatures

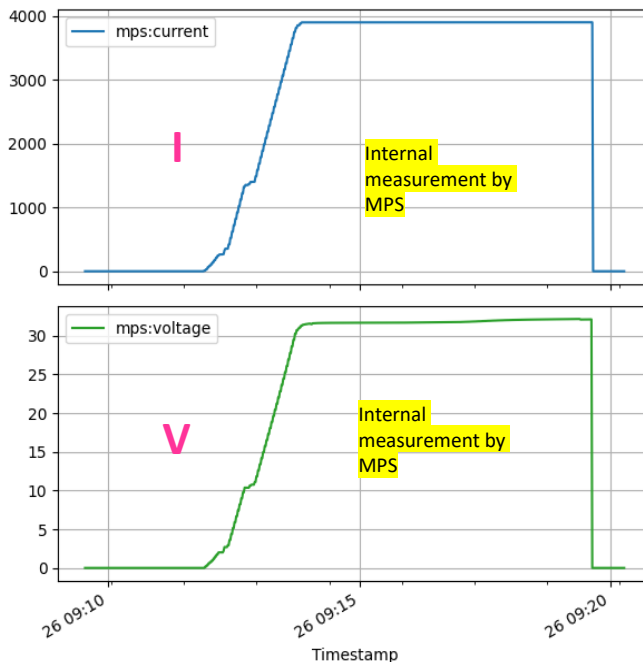
RTD## RTD## RTD##

reset PLC interlocks

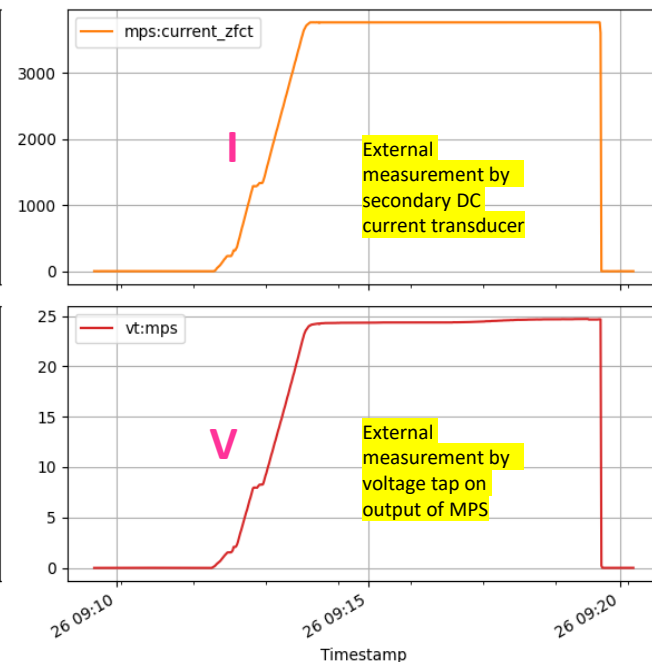
reset communication

set to local

Work-in-progress Phoebus screen for MPS controls



Offset and gain not corrected between int. current and ext. current measurement data



Offset and gain not corrected between int. voltage and ext. voltage measurement data

MPS for TM3 passed Site Acceptance Test

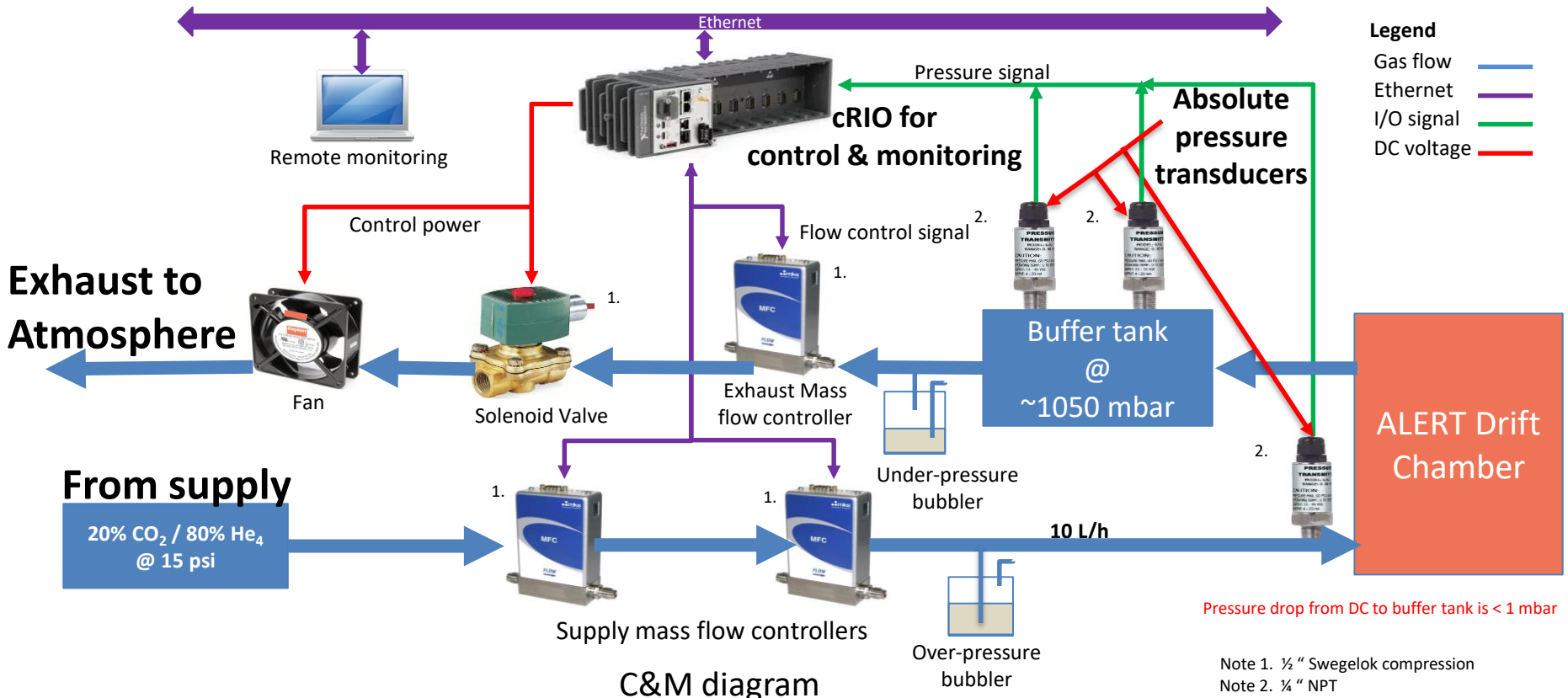


Hall B – ALERT



Marc McMullen, Brian Eng, Mindy Leffel, and George Jacobs

- Designing and developing C&M LabVIEW software for pressure-controlled He₄ CO₂ (80%:20%) gas supply
 - Specifying components
 - Fabricating and installing control equipment and cabling
 - Control designed to maintain supply flow (10 L/h) to the drift chamber
 - Control designed to maintain ~1050 mbar in the buffer tank by controlling the exhaust MFC, solenoid, and fan



Hall C – NPS

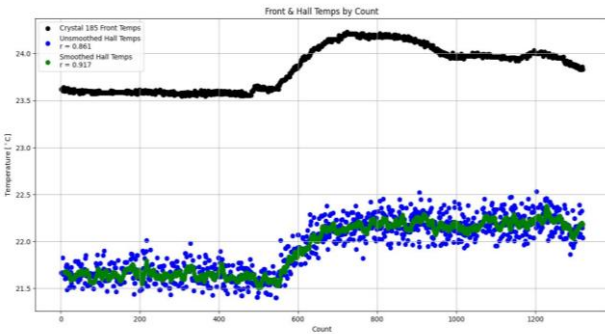


Aaron Brown, Mary Ann Antonioli, Marc McMullen, Brian Eng, Mindy Leffel, and Tyler Lemon

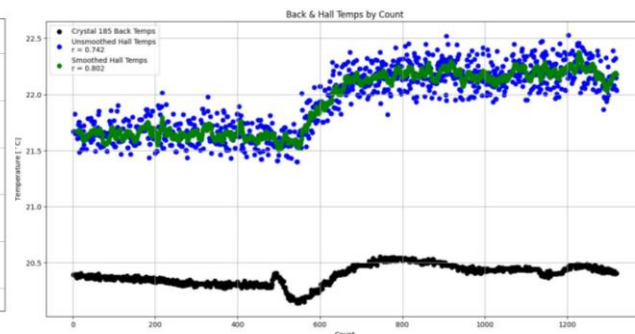
- Analysis of the temperature data archived in MYA shows front and rear crystal temperatures increase when ambient temperature does
- Pearson correlation coefficient r for crystals 185 shows

$$r = \begin{cases} 0.9 & \text{between the front sensor and ambient temperature} \\ 0.8 & \text{between the rear sensor and ambient temperature} \\ 0.8 & \text{between the front sensor and the rear sensor temperature} \end{cases}$$

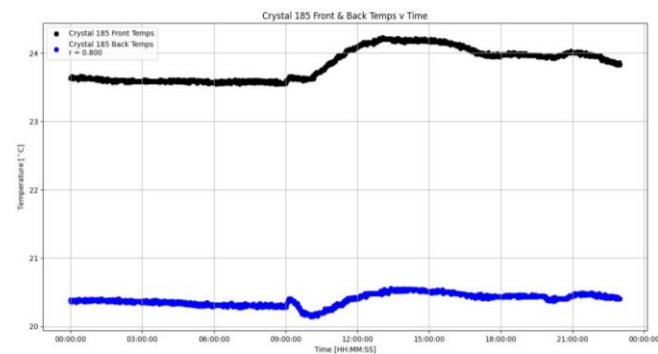
r values indicate a strong positive correlation
(as predicted by ANSYS simulation)



Plot of front sensor and ambient temperatures for crystal 185



Plot of rear sensor and ambient temperatures for crystal 185



Plot of front and back sensor temperatures for crystal 185

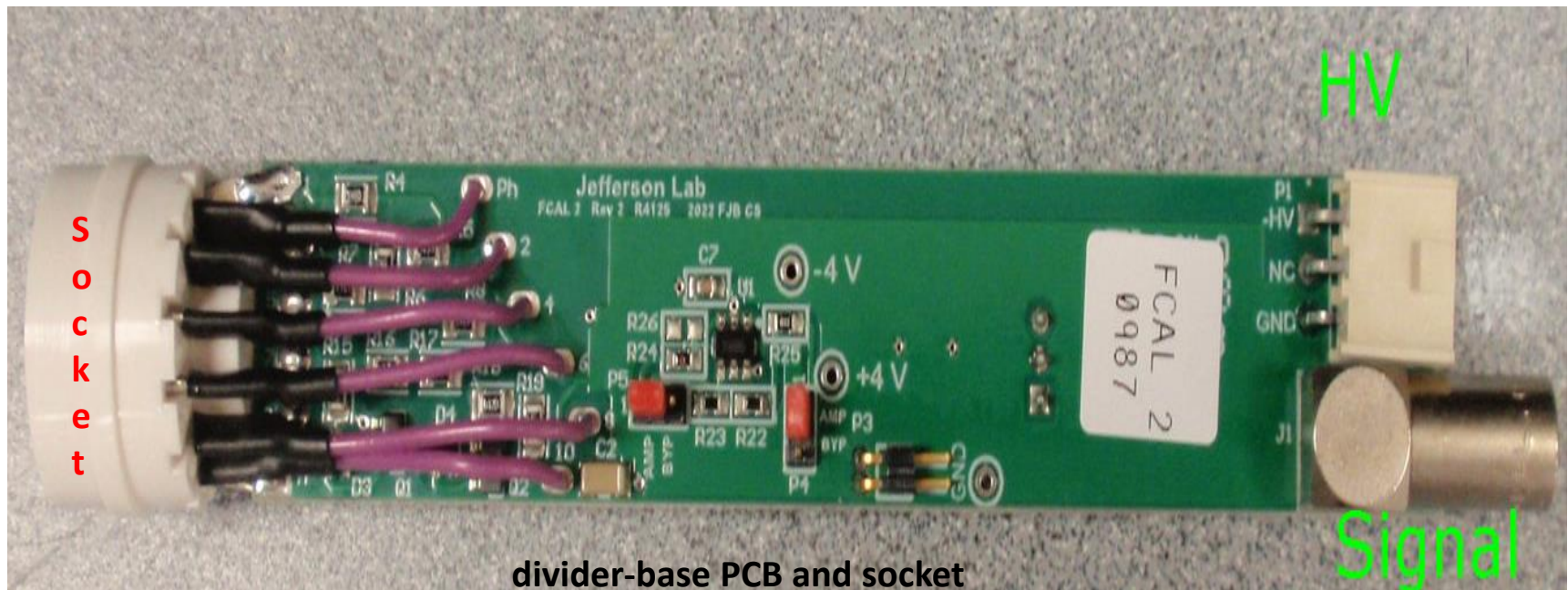
Hall D – FCAL 2

Aaron Brown, Mindy Leffel, George Jacobs

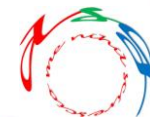


- **Fabricating and testing sockets**
 - Soldering wires to sockets to provide HV to different dynodes and the photocathode
 - Fabricated 1550 of 1650 sockets
 - About 100 divider-board + socket left to fabricated
- **Testing of divider-base + socket aborted**
 - Regulators are being removed as they are not rad-hard

Testing of 1650 divider base PCB + sockets needs to start again



divider-base PCB and socket



- Designing and developing laser interlock system for quartz bar quality assurance (QA) tests
 - Benchtop tests underway
 - Phoebus alarm handler under development to help monitor interlock signals
- Designing and developing photodiode readout PCB for QA tests
 - PCB designed and produced
 - Testing of PCB planned for after interlock system installation
- Designing and building shipping crates modifications to ship quartz bars from SLAC to JLab
 - As of February 5, 2024, modifications are complete to one of six crates
 - Work on all six crates will be complete by March 1, 2024



One of six shipping crates for transporting quartz bars from SLAC to JLab. Work on crate in photo is finished.

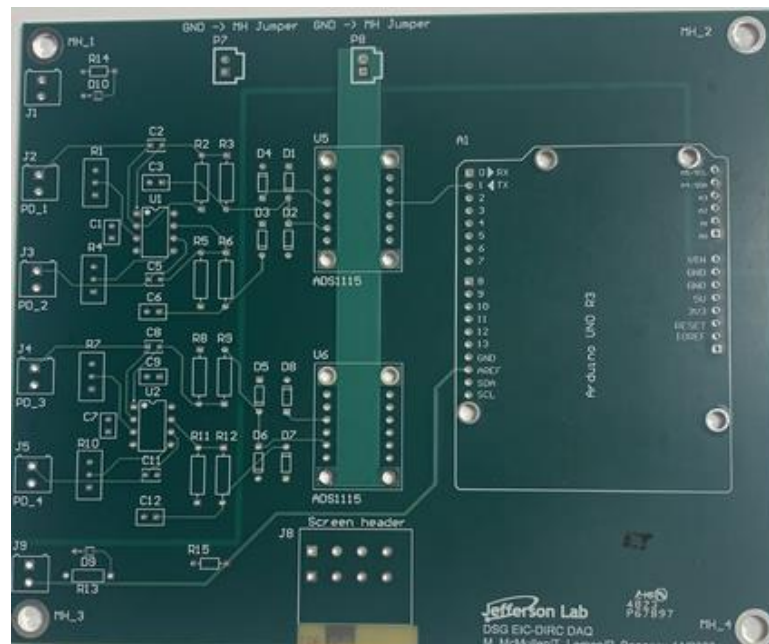


Photo of photodiode readout PCB designed by DSG.

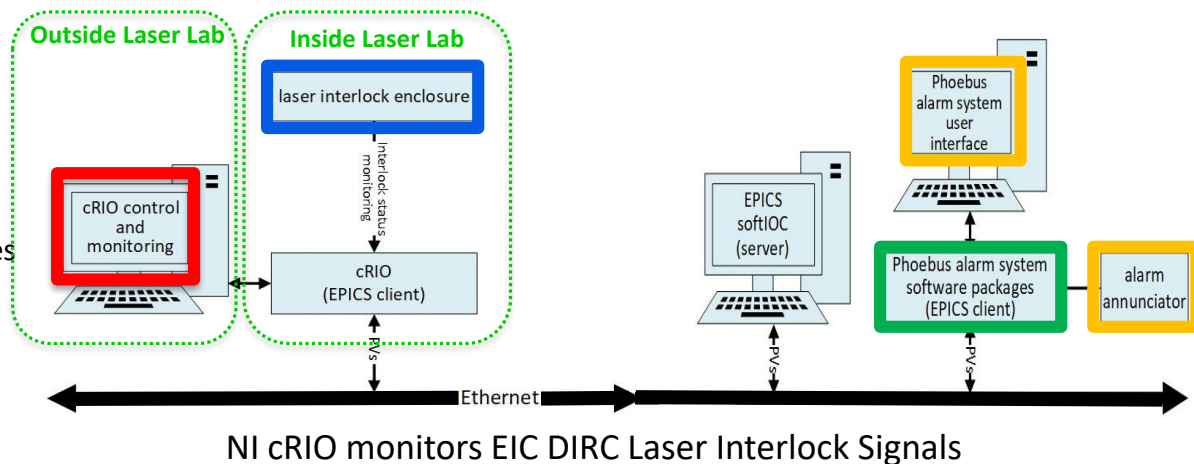
EIC - DIRC



Pete Bonneau, Tyler Lemon, Marc McMullen, and Mindy Leffel

Developing Phoebus alarm system test for EIC DIRC Laser Interlock

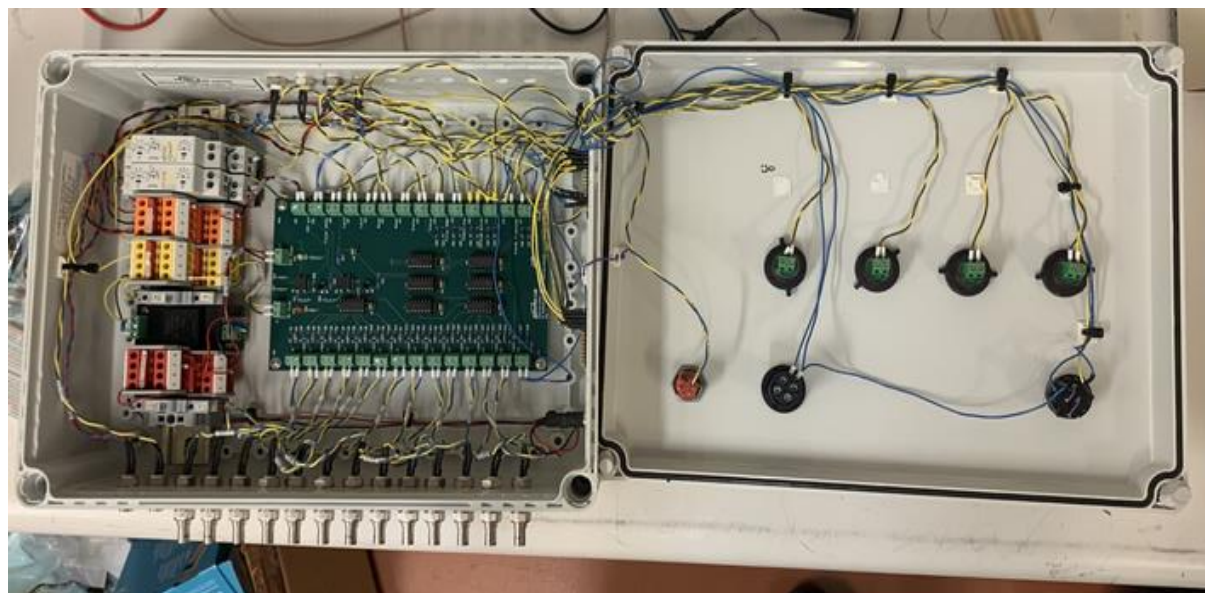
- NI **cRIO** monitors laser interlock status signals from **laser interlock** inside laser lab
- **Phoebus alarm system** monitors process variables (PVs) alarm status and reports alarms
- If a PV is in alarm state, latches PV value with timestamp
- Alerts users of alarms via **user interface** and **annunciator**



NI cRIO monitors EIC DIRC Laser Interlock Signals

Eight Boolean inputs to board:

1. Interior emergency stop chain button (on/off)
2. Exterior emergency stop button (on/off)
3. Laser area entry door status (open/close)
4. Laser area entry door status (redundant)
5. Optical table side wall position (up/down)
6. Sweep button engaged and sweep period not expired (yes/no)
7. Only exterior or interior control key in system (yes/no)
8. Configuration-dependent laser power-reducing filter in correct position (yes/no)



Laser interlock system's electronics designed and developed by DSG

DSG Note [2023-43](#) DSG Notes [2023-49](#) & [2023-56](#)



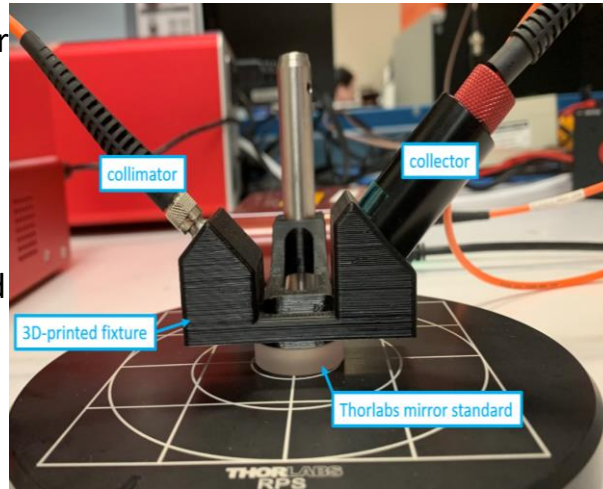
- EIC dual-radiator RICH and proximity-focusing RICH detectors generate Cherenkov light in UV spectrum (down to 200 nm)
 - Mirrors in detectors must reflect 95% of light

- Developing setup to test mirror reflectivity

- For $\lambda \in [200 \text{ nm}, 350 \text{ nm}]$
- Previous reflectivity test station used visible light $\lambda \in [400 \text{ nm}, 850 \text{ nm}]$

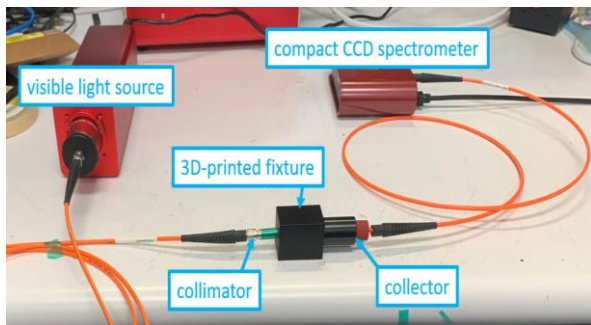
- Researched components, procured, and set up test stand for visible light—proof-of-concept test

- Measured reflectivity of Thorlabs mirror sample is close to its specified reflectivity

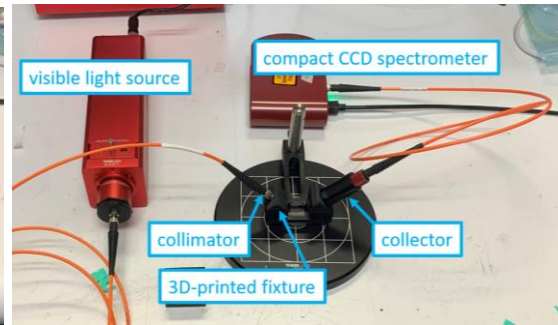


- Next step: set up for UV light

3 D Printed fixture

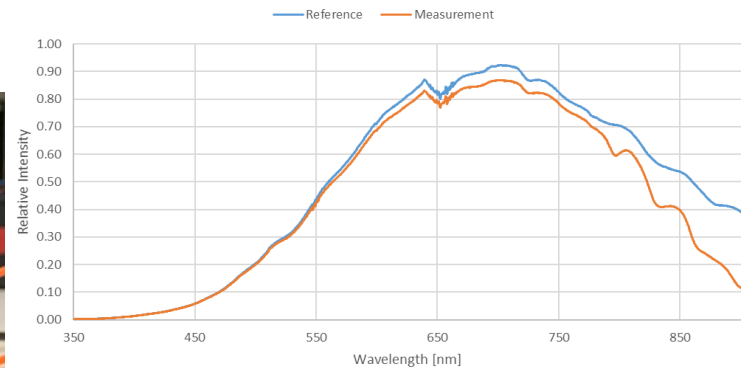


Setup for reference measurement. 3D printed fixture holds collimator to point directly at collector. Test beam does not reflect off of any mirror.



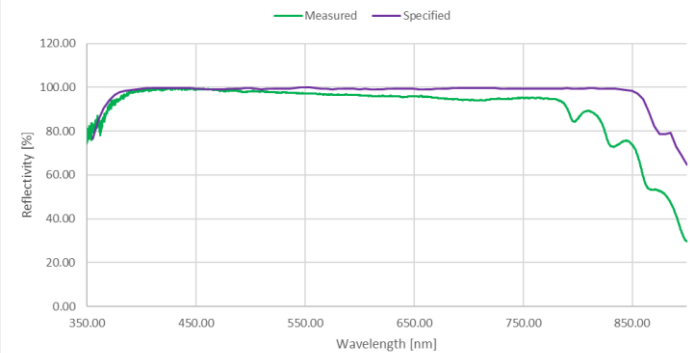
Test station set up for reflectivity measurement. 3D printed fixture holds collimator over mirror at 45°. Test beam reflects off of mirror and onto collector.

Raw Reflectivity Data for Thorlabs Mirror Standard



Raw data acquired using CCD. Blue trace is reference measurement (test beam collected directly). Orange trace is reflectivity measurement (test beam reflects off of mirror at a 45° angle of incidence).

Reflectivity of Thorlabs Mirror Standard



Specified reflectivity (purple). Calculated reflectivity (green by dividing measurement (orange trace) by reference data (blue trace) in the figure at the top

Conclusions

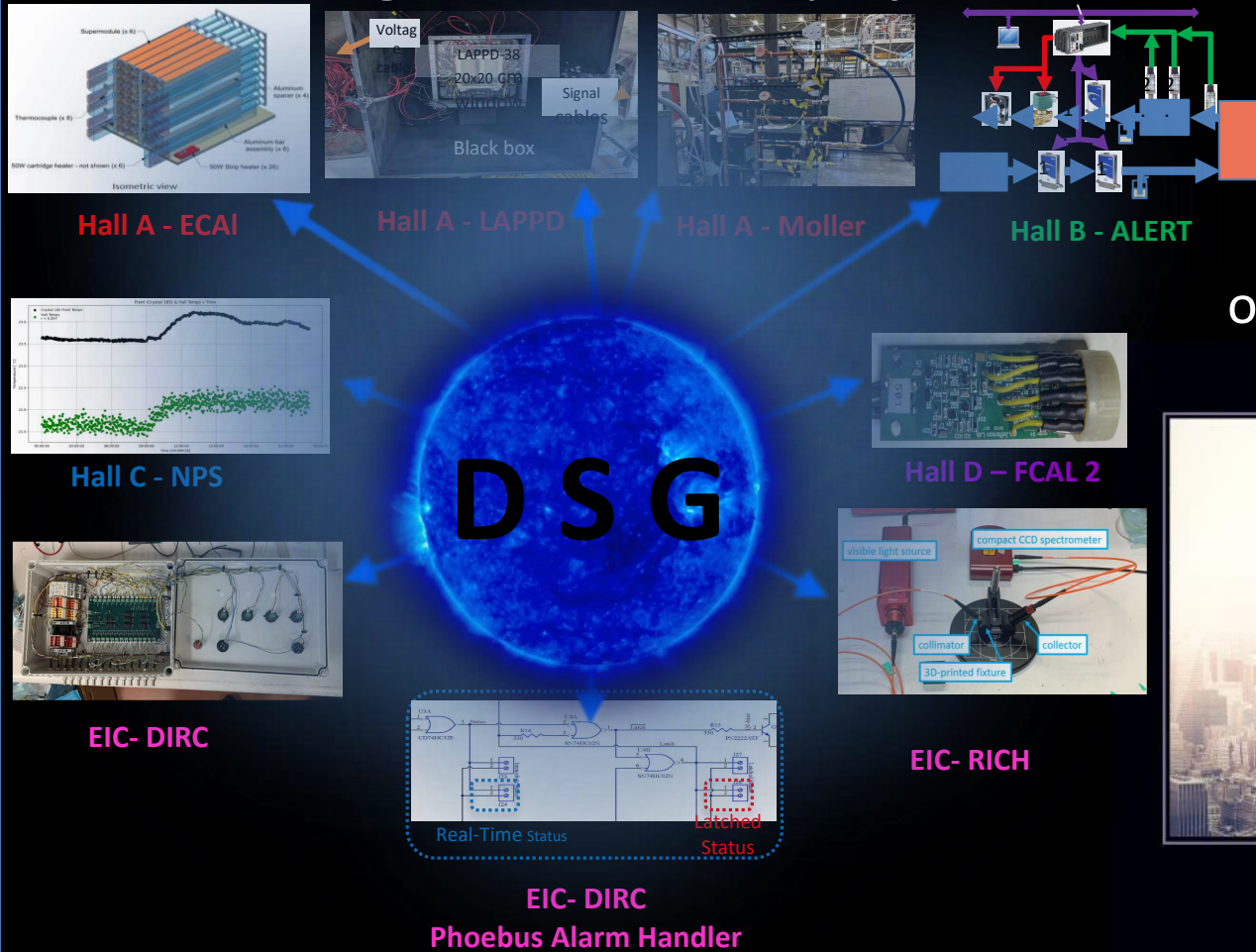
Contributing to several new projects

DSG staff

Jlab physics division's
leaders in

C&M

of detectors and magnets



LEADERS

OUR BURDEN IS TO TAKE BOLD, DRAMATIC ACTIONS.
SHOULD THOSE FAIL, YOUR BURDEN IS TO SUFFER BOLD, DRAMATIC CONSEQUENCES.

Thank you for your attention

